

Application No.: 10/530,611  
Amendment Dated: September 19, 2007  
Reply to Office Action of: June 21, 2007

ARGM-108US

**Remarks/Arguments:**

Applicants note that the Examiner appears to be rejecting the claims, as originally filed. An Article 19 Amendment was, however, filed on April 7, 2005, amending the original claims. Applicants request that the Article 19 Amendment be entered. Applicants have attached the Article 19 Amendment for the Examiner's reference.

The claims of the original Application were rejected under 35 U.S.C. § 102 (b) and 35 U.S.C. § 103 (a) as being unpatentable in view of a combination of references to Yoshioka et al., Shiuan et al., Chee and Ciolac.

This Amendment is, however, being filed in response to the current Office Action dated June 21, 2007 as if the Article 19 Amendment was entered. Thus, claims 1 and 16-17 are pending in the above-identified application. Claims 2-15 have been cancelled. New claims 16 and 17 have been added.

Claim 1 is amended to recite,

... a **single video memory** for storing image data at an original resolution; and

... a controlling unit for outputting the image data **from said the single video memory** to:

(a) **each of the plurality of display apparatuses at a lower resolution** upon the plurality of display apparatuses being connected, the lower resolution being lower than the original resolution, and

(b) one of the plurality of display apparatuses at the original resolution upon the one of the display apparatuses being connected. (Emphasis added).

Basis for these amendments may be found, for example, in the specification at page 8, lines 14-17, page 9, lines 28-31, page 10, lines 3-12 and Fig 3.

In Applicants' exemplary embodiment, data to be displayed on a display apparatus is stored at an original resolution (i.e. data assigned to layers L1 to L8) in

VRAM 130. (Page 9, lines 28-31 and Fig. 3). That is, the exemplary display controlling apparatus includes "...a **single video memory** for storing image data at an original resolution..." The controlling unit 120 then outputs the data stored in the VRAM 130 to the display apparatuses. (Page 8, lines 14-17). That is, the controlling unit is for "...outputting the image data **from said the single video memory**..."

The controlling unit 120 may include a drawing processor 121 which determines the resolution of the contents to be displayed by the display apparatuses depending on how many of said display apparatuses are connected. (Page 10, lines 3-5). For example, if the drawing processor 121 determines that two display apparatuses are connected to the data output units, the drawing processor 121 reduce the resolution of each of the contents to be displayed by the display apparatuses to one-half the original resolution (lower resolution) stored in the VRAM 130. (Page 10, lines 6-9). That is, the controlling unit 120 outputs the image data **from said the single video memory** to "...each of the plurality of display apparatuses at a lower resolution upon the plurality of display apparatuses being connected..."

The Examiner admits that Yoshioka does not disclose adjusting the resolutions of each of said contents to be displayed on the basis of the number of display apparatuses. The Examiner argues, however, that Shiuan et al. teaches a dual display system where one display is running in high resolution mode and the other display is running in low resolution mode. (Page 8, lines 6-12).

Shiuan et al. stores data in two original resolutions (a low resolution and a high resolution) in the video memory of the display FIFO. (Para. [0031]). Shiuan et al. also includes a first display device 17 and a second display device 19. (Fig. 1). One of the display devices is selected to run in the original low resolution mode. The other display device is selected to run in the original high resolution mode. (Para. [0034]). That is, each display device displays the data in a respective **different original resolution**. Thus, the image data from the video memory in Shiuan et al. is not output to "...each of the plurality of display apparatuses at a lower resolution," as recited in claim 1.

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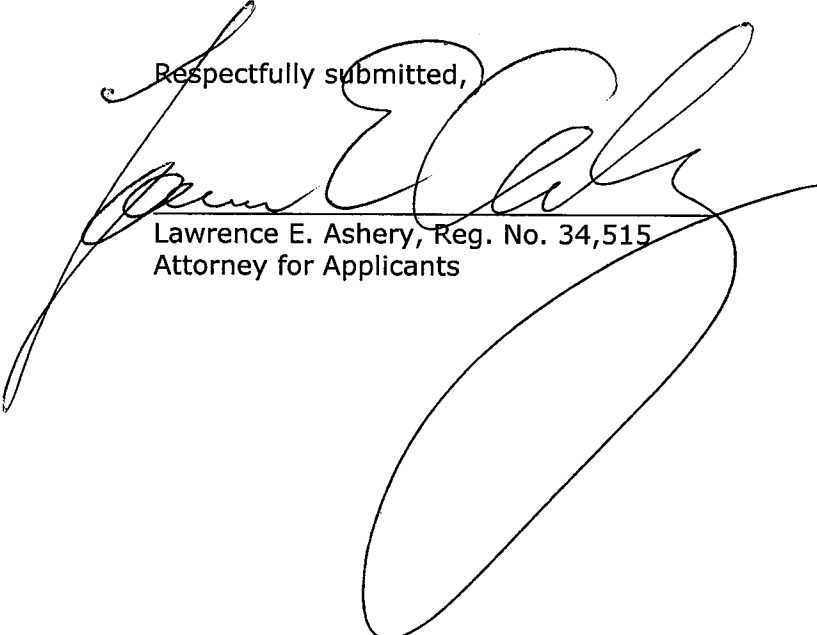
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Chee and Ciolac also do not disclose the features of claim 1. Thus, claim 1 is allowable over the art of record.

New claims 16 and 17 have been added. Basis for claim 16 may be found, for example, in the specification at page 10, lines 3-6. Basis for claim 17 may be found, for example, in the specification at page 10, lines 6-12. No new matter has been added.

In view of the amendments and arguments set forth above, the above-identified application is in condition for allowance, which action is respectfully requested.

Respectfully submitted,



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